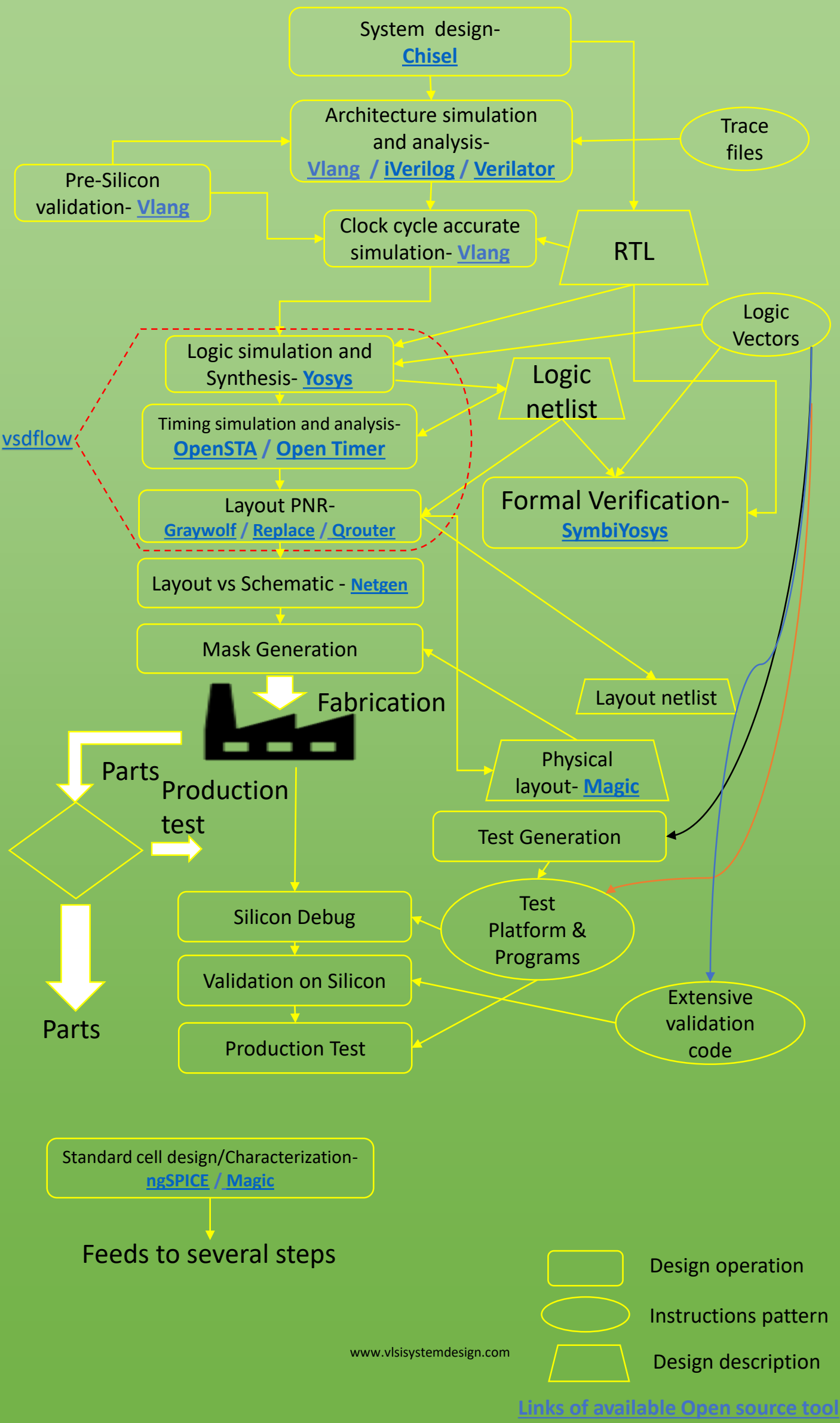Open Source Digital Chip design flow